NPN Triple Diffused Planar Silicon Transistor



2SC5302

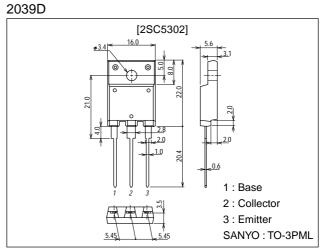
Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

Features

- · Fast speed (t_f =100ns typ).
- \cdot High breakdown voltage (V_{CBO} = 1500V).
- \cdot High reliability (adoption of HVP process).
- \cdot Adoption of MBIT process.

Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Sşmbo	Condition	Rating	Uni
Collector-to-Base Voltage	СВО		15⁄00	
Collector-to-Emitter Voltage	CEO		800	
EMitter-to-Base Voltage	EBO		6\	(
Cbllector Current	С		18	
Cbllector Current (pulse)	CP		38	
Cellector Dissipation	С		31	V
		Tc=25°C	750	
Jjunction Temperature	Т		150	°C
Stoprage Temperature	Tst		-55 to +150	°C

Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			mjøn	tyc	ma	
Cbllector Cutoff Current	CES	V _{CE} =1500V, R _{BE} =0			1A	m
Collector Sustain Voltage	CEO(sus)	I _C =100mA, I _B =0	80/			
Emitter Cutoff Current	EBO	V _{EB} =4V, I _C =0			1A	m
Cbllector Cutoff Current	СВО	V _{CB} =800V, I _E =0			1A	μ
DC Current Gain	h _{FE} 1V	CE=5V, IC=0.0A	20		3	
	h _{FE} 2V	CE=5V, IC=42A			7	

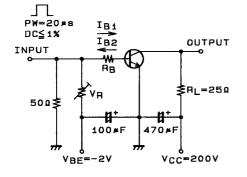
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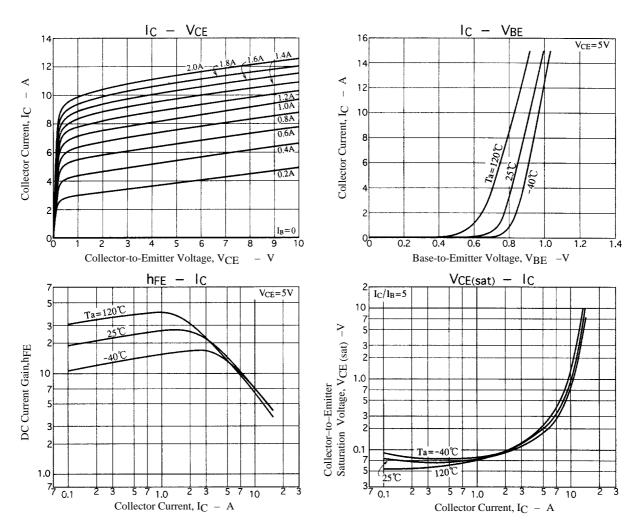
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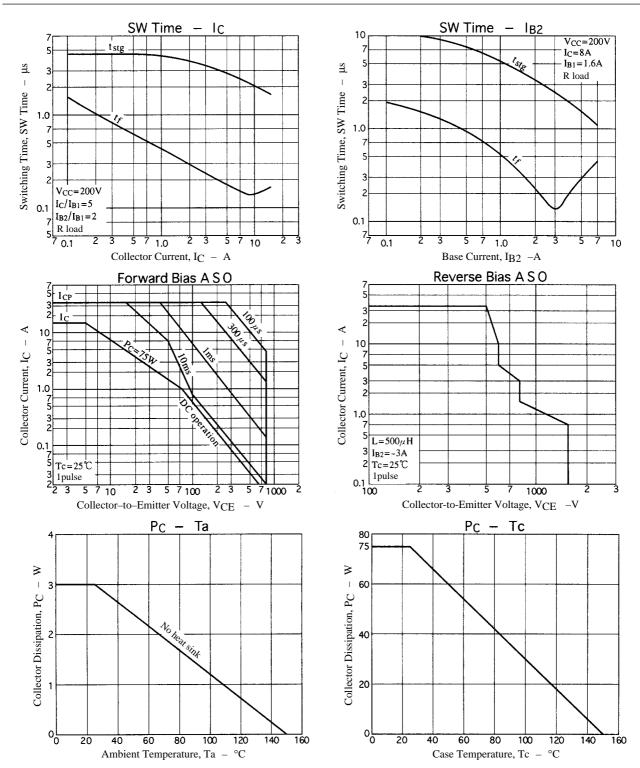
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Parameter	Symbo	Condition	Ratings			Unit
			mipn	tyc	ma	Onit
Collector-to-Emitter Saturation Voltage	CE(sat)	I _C =12A, I _B = 5 A				V
Base-to-Emitter Saturation Voltage	BE(sat)	I _C =12A, I _B = 5 A			1.V	
Storage Time	stg	I _C =8A, I _B 1=1.6A, I _B 2 0 -3.2A			3s	μ
Fall Time	f	I _C =8A, I _B 1=1.6A, I _B 2 2 -3.2A			0s	μ

Switching Time Test Circuit







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