

## MOS FIELD EFFECT TRANSISTOR

# 2SK2371, 2372

### SWITCHING N-CHANNEL POWER MOS FET

#### DESCRIPTION

These products are N-Channel MOS Field Effect Transistors designed for high voltage switching applications.

#### **FEATURES**

- Low on-state resistance 2SK2371: RDs (on) = 0.25  $\Omega$  MAX. (Vgs = 13 V, Ip = 10 A) 2SK2372: RDs (on) = 0.27  $\Omega$  MAX. (Vgs = 13 V, Ip = 10 A)
- Low input capacitance C<sub>iss</sub> = 3600 pF TYP.
- High Avalanche Capability Ratings

#### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^{\circ}C$ )

Drain to Source Voltage (Vgs = 0 V) (2SK2371/2372)	VDSS	450/500	V
Gate to Source Voltage (VDs = 0 V)	Vgss	±30	V
Drain Current (DC)	D (DC)	±25	А
Drain Current (pulse)*	D (pulse)	±100	А
Total Power Dissipation ( $T_c = 25^{\circ}C$ )	<b>P</b> T1	160	W
Total Power Dissipation ( $T_A = 25^{\circ}C$ )	Рт2	3.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C
Single Avalanche Current**	las	25	А
Single Avalanche Energy**	Eas	446	mJ
* PW $\leq$ 10 $\mu$ s, Duty Cycle $\leq$ 1 %			

\*\* Starting T<sub>ch</sub> = 25°C, R<sub>G</sub> = 25  $\Omega$ , V<sub>GS</sub> = 20 V  $\rightarrow$  0

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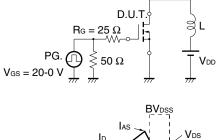
The mark <R> shows major revised points.

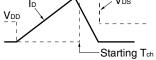
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

ELECTRICAL CHARACTERISTICS  $(T_A = 25^{\circ}C)$ 

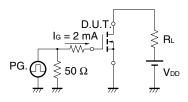
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS	
Drain to Source On-Resistance	RDS (on)		0.20	0.25	Ω	$V_{GS} = 10 V$	2SK2371
			0.23	0.27		ID = 13 A	2SK2372
Gate to Source Cutoff Voltage	VGS (off)	2.5	3.0	3.5	V	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	
Forward Transfer Admittance	y <sub>fs</sub>	8.0	13		S	Vds = 10 V, Id = 13 A	
Drain Leakage Current	loss			100	μA	$V_{\text{DS}} = V_{\text{DSS}}, V_{\text{GS}} = 0$	
Gate to Source Leakage Current	lgss			±100	nA	$V_{GS}=\pm 30~V,~V_{DS}=0$	
Input Capacitance	Ciss		3600		pF	V <sub>DS</sub> = 10 V	
Output Capacitance	Coss		700		pF	V <sub>G</sub> s = 0	
Reverse Transfer Capacitance	Crss		50		pF	f = 1 MHz	
Turn-On Delay Time	td (on)		40		ns	ID = 13 A	
Rise Time	tr		70		ns	V <sub>GS</sub> = 10 V	
Turn-Off Delay Time	td (off)		160		ns	Vdd = 150 V	
Fall Time	tr		60		ns	$R_G = 10 \Omega$	
Total Gate Charge	QG		95		nC	ID = 25 A	
Gate to Source Charge	Q <sub>GS</sub>		20		nC	V <sub>DD</sub> = 400 V	
Gate to Drain Charge	Qgd		40		nC	Vgs = 10 V	
Body Diode Forward Voltage	VF (S-D)		1.0		V	IF = 25 A, VGS = 0	
Reverse Recovery Time	trr		500		ns	IF = 25 A, VGS =	0
Reverse Recovery Charge	Qrr		4.5		μC	di/dt = 50 A/µs	

#### Test Circuit 1 Avalanche Capability

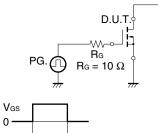




#### Test Circuit 3 Gate Charge



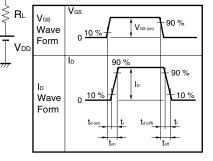
#### Test Circuit 2 Switching Time

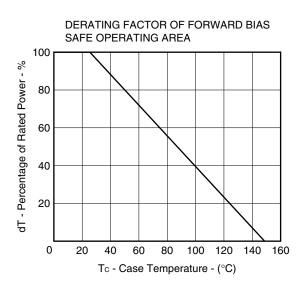


t

Duty Cycle  $\leq 1\%$ 

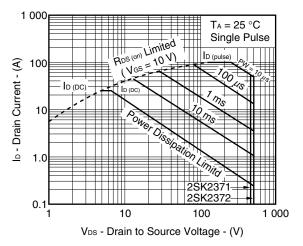
t = 1us



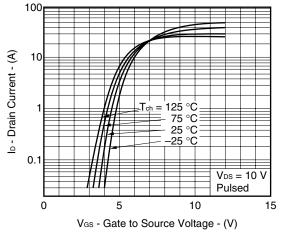


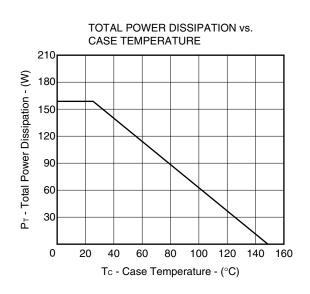
TYPICAL CHARACTERISTICS  $(T_A = 25^{\circ}C)$ 



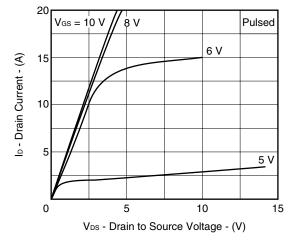


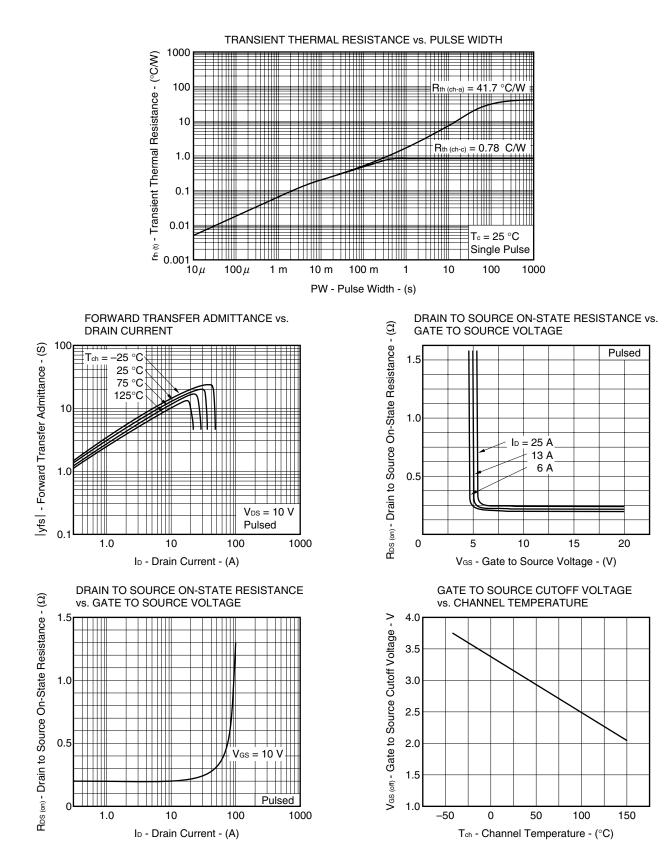
DRAIN CURRENT vs. GATE TO SOURCE VOLTAGE

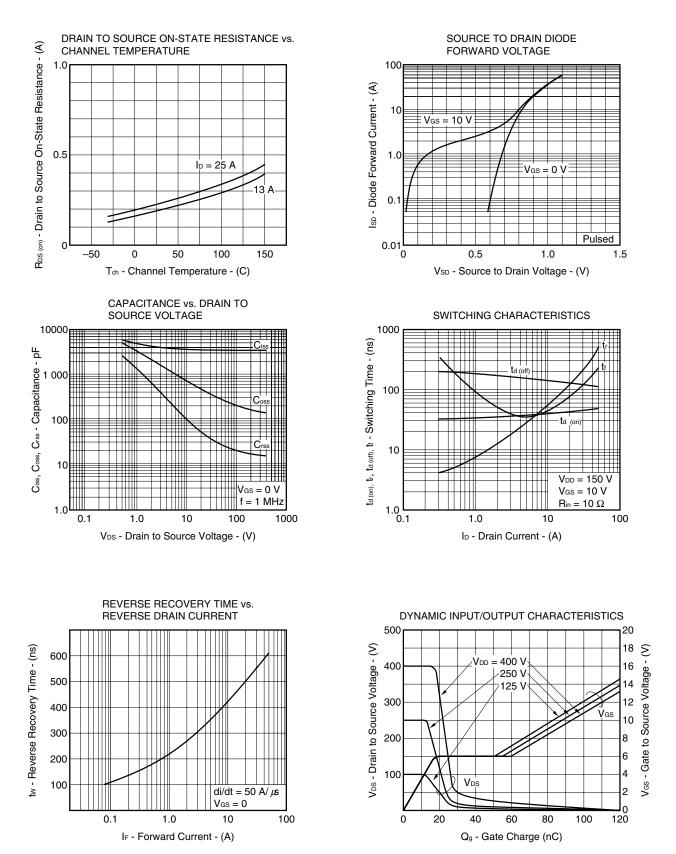


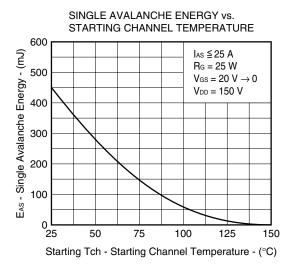


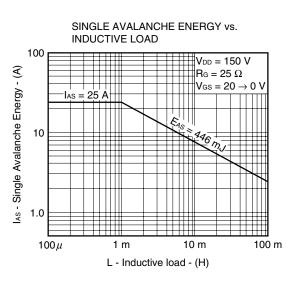








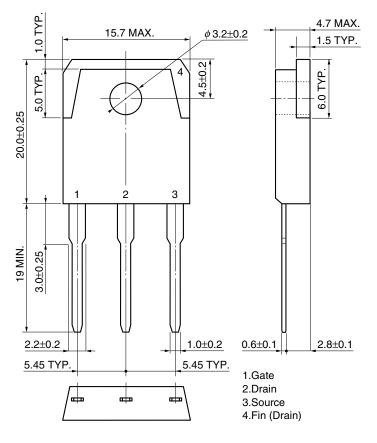




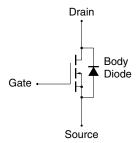
#### PACKAGE DRAWING (Unit: mm)

#### <R>

TO-3P (MP-88)



#### EQUIVALENT CIRCUIT



**Remark** Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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